

ABSTRACT OF THE DISCLOSURE

An image display has gate-lines and signal-lines in a matrix shape, and has thin film transistors each having: an island-shaped semiconductor layer having source region, drain region, and channel region sandwiched between them; a first insulation film formed between the semiconductor layer and a gate electrode of the same layer as that of the gate-lines; an interlayer insulation film formed above the semiconductor layer; and a source electrode and a drain electrode existing in the same layer as that of the signal-lines. Each capacitor has: a storage electrode of the same layer as that of the gate-lines; an insulation film formed on the storage electrode and being in contact therewith; and an electrode formed on the insulation film and being in contact therewith and existing in the same layer as that of the signal-lines.

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